## Locality, Matrix Multiplication, and Affine Transformations

#### **Advanced Compiler Techniques**

Source:

https://www.youtube.com/playlist?list=PLf3ZkSCyj1tf3rPAkOKY5hUzDrDoekAc7 Videos: 125 - 130

#### Part 1: Locality

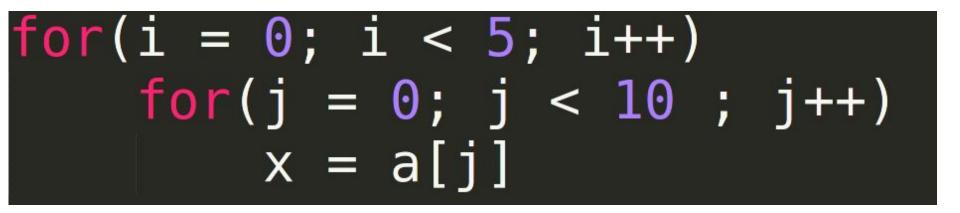
#### Locality: What is it? Why do we want it?

- Data locality refers to data accessed being near to each other, either in the spatial dimension, or the temporal dimension.
- Spatial locality: Addresses which are spatially near each other get accessed. Example: A-1, A, A+1, A+2, etc.
- Temporal locality: Same address is accessed again and again. Example: A, A, B, A, C, A, etc.
- Caches exploit locality to reduce the (average) memory latency observed by the execution pipeline.
- Typically more locality = More hits in caches = Faster Execution!

Interchanging Loops can affect locality

# for(i = 0; i < 5; i++) for(j = 0; j < 10; j++) x = a[j]</pre>

#### Reordering Loops can affect locality

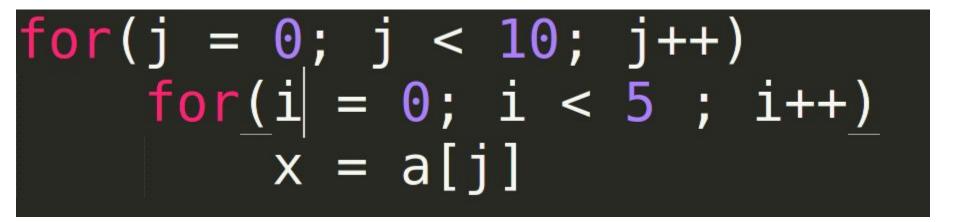


Miss

High Spatial locality, low Temporal locality

| a[6] a[7] a[8] | a[9] | a[0] | a[1] |
|----------------|------|------|------|
|----------------|------|------|------|

#### Reordering Loops can affect locality



Miss

High Spatial locality, high Temporal locality

| a[0] | a[1] |  |  |  |  |
|------|------|--|--|--|--|
|------|------|--|--|--|--|

#### What parameters affect locality?

- Spatial: Prefetchers + CL size
- Temporal: Replacement Policy

#### Part 2: Matrix Multiplication

C[row, col]: Can be reg allocated. One time cost.

- A[row, idx]: No point in reg allocating. Might need to pay for every access.
- B[row, idx]: No point in reg allocating. Might need to pay for every access.

Worst case: A -> Column major, B -> Row major Best case: A -> Row major, B -> Column major

Realistically, if both A and B are row major, and B\_COL\_MAX is sufficiently large, every access to B[idx, col] misses in cache.  $O(n^3)$  misses.

However, if

- 1. There are more cache lines than there are rows in B
- 2. More than 1 element of B can completely in a cache line

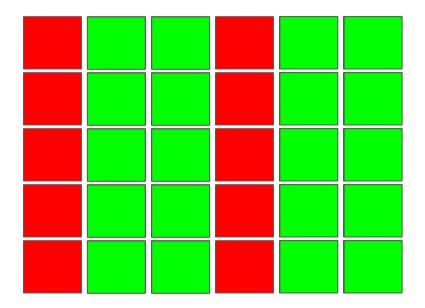
The inner two loops will only have to face  $O(n^2/C)$  misses when the first column would be accessed where C = Cache Line Size / Size of one element of B = number of columns of B that can fit in the cache.

In best case when we have sufficiently high number of cache lines, all columns of B will fit, reducing the total penalty to  $O(n^2/C)$  for all three loops

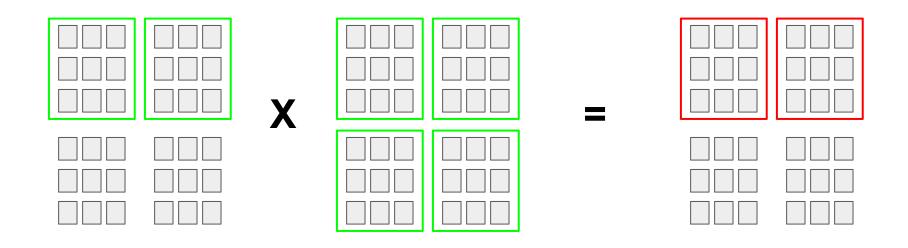
#### What about A and C?

Fastest moving index for both A and C is the columns. Therefore, if A and C are row-major, then we only need to pay  $O(n^2/C)$  for each of them. Therefore in total we pay around  $3n^2/C$ .

If C > n, we don't get any benefit as then useless data gets fetched into cache. If C = n, we pay around 3n penalty. This makes sense as we need n access for each matrix to bring the matrix into the cache. n accesses because each access brings a complete row, and there are n rows.



#### **Blocking Matrix Multiplication**



#### **Blocking Matrix Multiplication**

Best case, each block faces  $3B^2/C$  misses ( $B^2/C$  for each sub-matrix). If each element is one byte long:

C = cache line size = l

Total  $(n/B)^3$  block operations.

Therefore total cost =  $3n^3/BC$ 

B <= n and C <= n. Therefore, best case we get 3n penalty. This matches the best case estimate from earlier.

#### Part 3: Affine Loop Transformations

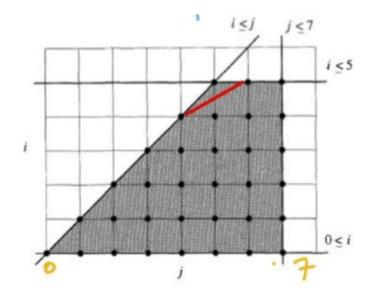
#### What does affine mean?

- An affine expression is a linear expression of the inputs.
- $f(x_1, x_2, x_3, \dots, x_n) = c_0 + c_1 x_1 + c_2 x_2 + \dots + c_n x_n$

#### When can we use polyhedral optimizations?

- Even if accesses are affine, there might be dependencies.
- Upper and lower bounds of loops are affine functions of outer loop variables.
- Increments are by 1. This can be achieved by using a placeholder variable in loop and multiplying it by a constant before use.
- Under this assumption, the iteration space will always be convex.

#### Example



#### Categories of Affine Transformations

- Splitting iteration space into independent slices which can be executed parallely.
- Blocking to create a hierarchy of iterations to improve locality.

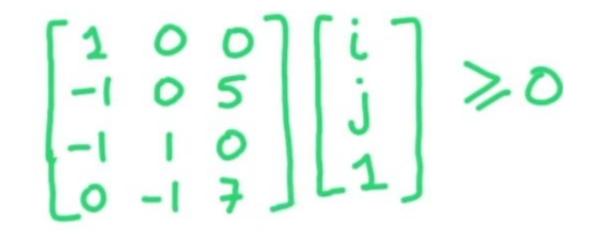
#### Example: Splitting Into Parallel Slices

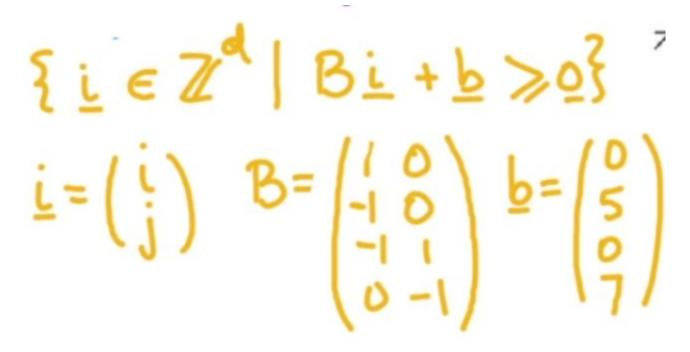
## for(idx = 0; idx < N; idx ++) a[i] = b[i]</pre>

#### Example: Splitting Into Parallel Slices

#### Affine Transform Theory: Three Spaces

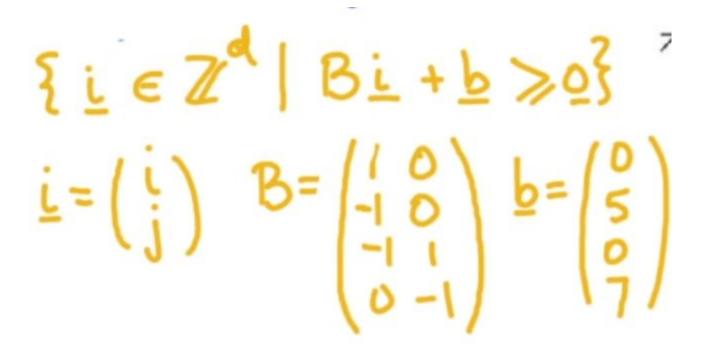
- 1. Iteration space: Set of all dynamic execution instances, i.e. all possible combinations of iterators. May/May not be rectangular.
- 2. Data space: Set of array elements accessed. Typically defined as an affine functions of the iteration space.
- 3. Processor space: Set of all processors in the system. We create an affine function map from iteration space to processor space.





#### Iteration Space Example: Execution order

• Fastest moving variable is lexicographically smaller.



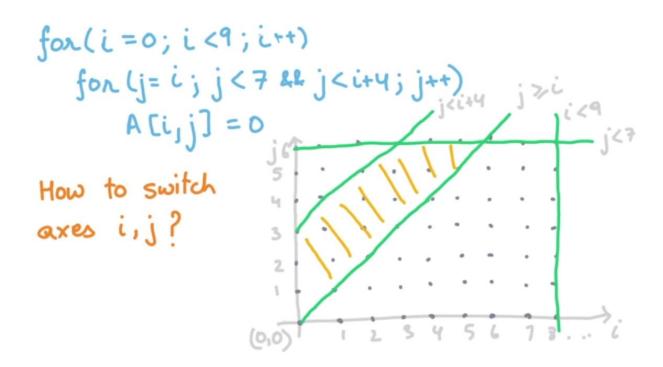
**Iteration Space: Loop Invariant** 

#### 



#### **Exchanging Variables**

• Suppose we want to exchange i and j



#### **Exchanging Variables**

- 1. Project the iteration space on j to find the range of j.
- 2. For each j, find i in terms of j.

We are guaranteed to have another convex polyhedron after projection.

#### **Exchanging Variables: Projection**

- The set of points (x<sub>1</sub>, x<sub>2</sub>, ..., x<sub>m</sub>) will be in the projection of S on m dimensions if for some (x<sub>m+1</sub>, x<sub>m+2</sub>, ..., x<sub>n</sub>), (x<sub>1</sub>, x<sub>2</sub>, ..., x<sub>n</sub>) lies in S.
- Effective for every point in the projection, you should be able to find some set of values for the rest of n-m dimensions such that the n dimensional point lies in S.

#### Exchanging variables: Idea

- Project the iteration space onto all the dimensions except the desired innermost variable's dimension.
- Then project the rest onto all the dimensions except the desired second-innermost variable's dimension.
- Then project the rest onto all the dimensions except the desired third-innermost variable's dimension.

... and so on until all variables are exhausted.

#### Fourier Motzkin Method

Input:

- 1. A convex polyhedron S in m dimensions.
- 2. A variable  $x_m$  to eliminate

Output:

S', a projection of S on all dimensions except the m<sup>th</sup> dimension.

#### Fourier Motzkin Method: Terminology

## S = { x | Bx+b>0} C = constraints in S that involve 2m (coefficient of xm =0)

#### Fourier Motzkin Method: Algorithm

Algorithm:  $S = \{ \underline{x} \mid \underline{B}\underline{x} + \underline{b} \ge 0 \}$   $C = constraints in S involving x_m$ For every pair of lower bound and upper bound on xm in C such that  $L \leq C_1 * \chi_m$ C1, C, ≥0  $C_2 \chi_m \leq U$ add C2 L ≤ C1 U to S' Also add S-C to S'

Fourier Motzkin Method: Example Setup for(i=0; i<9; i++) for (j=i; j< min(7, i+4); j++) A [i, j] = 0  $\underline{i} = \begin{pmatrix} i \\ j \end{pmatrix}$ S= { i | B i + b ≥ 0}  $B = \begin{pmatrix} 1 & 0 \\ -1 & 0 \\ -1 & 1 \\ 0 & -1 \\ 0 & -1 \end{pmatrix} \qquad b = \begin{pmatrix} 0 \\ 8 \\ 0 \\ 6 \\ 2 \end{pmatrix} \qquad \text{Eliminate } i \\ (project on j)$ 

#### Fourier Motzkin Method: Example Solution

$$C = \{ \underbrace{i}_{i} \mid B_{c} \underbrace{i}_{i} + b_{c} \ge 0 \}$$

$$B_{c} = \begin{pmatrix} 1 & 0 \\ -1 & 0 \\ -1 & 1 \\ 1 & -1 \end{pmatrix} \qquad b_{c} = \begin{pmatrix} 0 \\ 8 \\ 0 \\ 3 \end{pmatrix}$$

$$I = \underbrace{i}_{i} \ge 0 \qquad \text{consider} \qquad 0 \le 8 \quad (1,3)$$

$$O \le 8 \quad (1,3)$$

$$O \le j \quad (1,4)$$

$$2 \cdot i \ge j - 3 \qquad \text{pair} \qquad j - 3 \le 8 \quad (2,3)$$

$$3 \cdot i \le 8 \qquad 0 \\ 4 \cdot i \le j \qquad \le x \geqslant \qquad j - 3 \le j \quad (2,4)$$

#### **Example Solution**

### Finally: $S' = j \ge 0$ $j \le 6$ (from S-C)

## Thanks!

-Setu Gupta